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Docket No.: CIS0203US

December 4, 2003

MAIL STOP PATENT APPLICATION  
COMMISSIONER FOR PATENTS  
P. O. BOX 1450  
ALEXANDRIA, VA 22313-1450

22141 U.S. PTO  
10/727381



Enclosed herewith for filing is a patent application, as follows:

Inventor(s): Gopakumar Parameswaran, Cuong C. Ly, Douglas L. Yanagawa, Mark N. Yamashita and Yuval Bachar

Title: SYSTEM AND METHOD FOR REDUCING CROSSTALK BETWEEN VIAS IN A PRINTED CIRCUIT BOARD

<u>X</u>	Return Receipt Postcard
<u>X</u>	This Transmittal Letter (1 page) (in duplicate)
<u>13</u>	page(s) Specification (not including claims)
<u>10</u>	page(s) Claims
<u>1</u>	page Abstract
<u>5</u>	Sheet(s) of Drawings ( <i>Figures 1A, 1B; 2A, 2B; 3; 4, 5 and 6</i> )
<u>15</u>	page(s) Declaration For Patent Application and Power of Attorney ( <i>executed-in-counterpart</i> )
<u>1</u>	page(s) Recordation Form Cover Sheet (in duplicate)
<u>15</u>	page(s) Assignment ( <i>executed-in-counterpart</i> )
<u>1</u>	page(s) NonPublication Request
<u>1</u>	page(s) Information Disclosure Statement
<u>1</u>	page(s) PTO Form 1449 citing 4 references
<u>X</u>	Copies of three (3) references

**CLAIMS AS FILED**

For	Number Filed		Number Extra		Rate		Basic Fee
Total Claims	44	-20 =	24	X	\$18.00 =	\$	770.00
						\$	432.00
Independent Claims	6	-3 =	3	X	\$86.00 =	\$	258.00
<input type="checkbox"/>	Fee of _____ for the first filing of one or more multiple dependent claims per application						\$
<input type="checkbox"/>	Fee for Request for Extension of Time						\$

Please make the following charges to Deposit Account 502306:

- ☒ Total fee for filing the patent application in the amount of \$ 1460.00  
☒ The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account 502306.

EXPRESS MAIL LABEL NO:

EV 304738421 US

Respectfully submitted,

*Brenna A. Brock*

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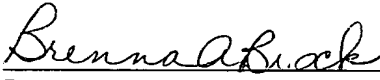
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MODIFIED PTO/SB/35 (11-00)

<b>REQUEST AND CERTIFICATION UNDER 35 U.S.C. 122(b)(2)(B)(i)</b>	Inventors	Gopakumar Parameswaran, Cuong C. Ly, Douglas L. Yanagawa, Mark N. Yamashita and Yuval Bachar
	Title	SYSTEM AND METHOD FOR REDUCING CROSSTALK BETWEEN VIAS IN A PRINTED CIRCUIT BOARD
	Atty Docket Number	CIS0203US

I hereby certify that the invention disclosed in the attached application **has not and will not be** the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing. I hereby request that the attached application not be published under 35 U.S.C. 122(b).

12-4-2003  
Date

  
Brenna A. Brock  
Attorney for Applicants  
Reg. No.: 48,509

This request must be signed in compliance with 37 CFR 1.33(b) and submitted with the application **upon filing**.

Applicant may rescind this nonpublication request at any time. If applicant rescinds a request that an application not be published under 35 U.S.C. 122(b), the application will be scheduled for publication at eighteen months from the earliest claimed filing date for which a benefit is claimed.

If applicant subsequently files an application directed to the invention disclosed in the attached application in another country, or under a multilateral international agreement, that requires publication of applications eighteen months after filing, the applicant **must** notify the United States Patent and Trademark Office of such filing within forty-five (45) days after the date of the filing of such foreign or international application. **Failure to do so will result in abandonment of this application (35 U.S.C. 122(b)(2)(B)(iii)).**